

Notice of References Cited	Application/Control No. 10/706,438	Applicant(s)/Patent Under Reexamination PARTSCH, TORSTEN	
	Examiner Michael B. McFadden	Art Unit 2188	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Sakamoto et al. "A Digitally Programmable Delay Chip with Picosecond Resolution". 19 September 1989. IEEE. Bipolar Circuits and Technology Meeting, 1989., Proceedings of the 1989. pp. 295-297.
	V	
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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